ABSTRACT

Disclosed is a method of forming a floating gate in a date flash memory device on which first and second polysilicon films are stacked. After the first polysilicon film is formed, a SiH₄ gas is introduced to decompose SiH₄ and SiO₂ into Si and H₂ and Si and O₂. A N₂ anneal process is then implemented so that the decomposed H₂ gas and O₂ gas react to a N₂ gas and are then outgassed. Next, a SiH₄ gas and a PH₃ gas are introduced to form the second polysilicon film. A native oxide film within the interface of the first polysilicon film and the second polysilicon film is removed to improve characteristics of the data flash memory device.

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